Cu-Catalyzed Vapor—Liquid—Solid Growth of SiGe Microwire Arrays with Chlorosilane and Chlorogermane Precursors

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ABSTRACT: Selected area Cu-catalyzed vapor—liquid—solid growth of SiGe microwires is achieved using chlorosilane and chlorogermane precursors. The composition can be tuned up to 12% Ge with a simultaneous decrease in the growth rate from 7 to 1 μm min⁻¹. Significant changes to the morphology were observed, including tapering and faceting on the sidewalls and along the lengths of the wires. Characterization of axial and radial cross sections with transmission electron microscopy revealed no evidence of defects at facet corners and edges, and the tapering is shown to be due to in situ removal of catalyst material during growth. X-ray diffraction and transmission electron microscopy reveal a Ge-rich crystal at the tip of the wires, strongly suggesting that the Ge incorporation is limited by the crystallization rate.

INTRODUCTION

Since the seminal work of Wagner and Ellis,¹ vapor—liquid—solid growth has been applied to the growth of a variety of semiconductor nano- and microwires. The unique properties of these high aspect ratio structures make them promising candidates for use in many types of devices, including photovoltaic, photoelectrochemical, optoelectronic, and solid growth has been applied to the growth of a variety of aligned, monodisperse arrays of microwires with micron-scale atmospheric pressure Cu-catalyzed vapor interface. In contrast to much of the existing work, lane and chlorogermane precursors. Similar to Si wire arrays, advantages for use in chemical vapor deposition growth and around the wire bases,²⁰ and very high growth rates²¹,²² compared to silane.²³ Finally, chloride based precursors are safer than hydrides, due to their lower toxicity and flammability.²⁴

We demonstrate atmospheric pressure, Cu-catalyzed, vapor—liquid—solid grown Si₁₋ₓGeₓ microwire arrays using chlorosilane and chlorogermane precursors. Similar to Si wire arrays, the Si₁₋ₓGeₓ microwire arrays can be grown over ∼1 cm² areas with high aspect ratio wires growing vertically from a patterned oxide on Si (111) substrates. The work presented here explores a Si₁₋ₓGeₓ wire size, growth temperature, and reactor pressure regime different from those explored previously. To the best of our knowledge, this is also the first report of Cu-catalyzed vapor—liquid—solid growth of these alloys with chloride precursors. An increase in the Ge precursor flow led to an expected increase in Ge content and an unexpected reduction of the growth rate. Ge incorporation was limited to ≤12%, unlike the full compositional tunability demonstrated for random Au-catalyzed growth of SiGe nanowires with silane and germane precursors.⁹,¹⁵ Consistent with the work of Soman et al. on thin film growth,²⁵ contamination from adventitious Ge species on reactor surfaces makes reproducibility more challenging than for pure silicon growth, and in our case contamination results in growth of disordered nano- and microwire forests instead of vertical microwires. Improved reproducibility was achieved by alternating SiGe wire growths with pure Si growth, in effect keeping the reactor in a steady state. The SiGe wire morphology, composition, and crystallinity were carefully monitored from run to run with scanning electron microscopy, energy-dispersive X-ray spectroscopy, and X-ray diffraction. Cross sections of single wires examined with transmission electron microscopy reveal a Ge-rich layer at the tip of the wire.

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calculated using the methodology described in Zhylik et al.26 and aligning to the (111) or (153) peak of the Si substrate. Relaxation was performed on a Panalytical X Pert Pro, with a hybrid X-ray mirror/2-Bounce monochromator and a 3 × 220 Ge analyzer with a sealed Xe proportional detector. Reciprocal space maps were collected after cleaving to 1 cm² pieces and loaded onto a boat in our atmospheric pressure hot wall chemical vapor deposition reactor at 750 °C. Typical growth parameters include 1 slm of H2 carrier gas, bubbled H2 carrier gas through a nominally 32 °C GeCl4 cylinder, and collection of the overpressure from a SiCl4 cylinder heated to 85 °C at a growth temperature of 1000 °C. We use molar flow rates of 0–98 μmol min⁻¹ GeCl4, 430 μmol min⁻¹ SiCl4, and 4 × 10⁴ μmol min⁻¹ H2. After growth, samples were cooled slowly to 750 °C inside the tube, then removed.

Scanning Electron Microscopy. Micrographs were collected using either a Hitachi S-4100 FE-SEM, an FEI Nova 600, or an FEI Nova 200 dual beam scanning electron microscope and focused ion beam by first aligning to the growth axis and then tilting to 30° off-normal incidence. The FEI Nova 200 was also used for energy dispersive spectroscopy of as-grown wire arrays for compositional verification using an Ametek EDAX Genesis 7000 and a Sapphire detector.

X-ray Diffractometry. High resolution X-ray diffractometry was performed on a Panalytical X’Pert Pro, with a hybrid X-ray mirror/2-Bounce monochromator and a 3 × 220 Ge analyzer with a sealed Xe proportional detector. Reciprocal space maps were collected after first aligning to the (111) or (153) peak of the Si substrate. Relaxation was calculated using the methodology described in Zhylik et al.26 and comparing the calculated composition to the composition measured using energy dispersive X-ray spectroscopy.

Transmission Electron Microscopy. Select areas of wire arrays were mechanically removed with a razor blade and dispersed in isopropyl alcohol. After drop casting the solution onto Si substrates and allowing the solvent to evaporate, a 20 nm layer of Al2O3 was deposited using atomic layer deposition. Radial and axial cross sections of single wires were then prepared using standard focused ion beam milling techniques in either a FEI Nova 600 or FEI Versa 3D dual beam system. Transmission electron microscopy characterization in this work was done on a JEOL 2100-F 200 kV FE-TEM and a FEI Technai TF30UT 300 kV FE-TEM.

EXPERIMENTAL SECTION

Wire Array Growth. High purity (6 N, Alfa Aesar) Cu-catalyst decorated Si (111) substrates were prepared with standard photolithographic liftoff processes as described elsewhere.21 Substrates were cleaved to 1 cm² pieces and loaded onto a boat in our atmospheric pressure hot wall chemical vapor deposition reactor at 750 °C. Typical growth parameters include 1 slm of H2 carrier gas, bubbled H2 carrier gas through a nominally 32 °C GeCl4 cylinder, and collection of the overpressure from a SiCl4 cylinder heated to 85 °C at a growth temperature of 1000 °C. We use molar flow rates of 0–98 μmol min⁻¹ GeCl4, 430 μmol min⁻¹ SiCl4, and 4 × 10⁴ μmol min⁻¹ H2. After growth, samples were cooled slowly to 750 °C inside the tube, then removed.

RESULTS AND DISCUSSION

Figure 1. Scanning electron micrographs of Si1−xGex wire arrays grown with increasing GeCl4 flow rates and constant SiCl4 flow for 10 min. The growth rate drops from 7 μm min⁻¹ at the lowest to 1 μm min⁻¹ at the highest [Ge]/[IV] ratio. Large sidewall facets are introduced gradually as the [Ge]/[IV] ratio increases until they dominate the sidewall morphology at [Ge]/[IV] = 18.6%.
deposition and in situ etching of the metal catalyst. Hannon et al. observed similar faceting in Si nanowires after introducing a growth interruption, which they attributed to catalyst migration between the first and second growth phase. Pure Si microwire arrays were grown at elevated temperatures of 1050 and 1100 °C to encourage etching and evaporation of the catalyst during the growth process. The resulting wire arrays had morphologies more or less identical to those observed for the SiGe wire arrays, clear evidence of increased catalyst removal with higher GeCl₄ flow rates. The sidewall faceting likely results from competing energetics when catalyst material is removed; cylindrical growth is normally stable, but the catalyst size, and therefore shape, is perturbed until it is energetically favorable for the catalyst diameter to reduce, returning to the preferred shape while creating nonvertical solid wire surfaces. A similar effect is likely responsible for the wide wire bases, due to the difference in the surface energy between the catalyst particle and the SiO₂ growth mask during initiation compared to the vapor environment during growth. Reducing the Si₀.₉₋ₓGeₓ growth temperature to 950 °C was also attempted, and resulted in a large increase in disordered growth compared to perfect, vertical wire array growth, and we could not fairly compare its effects on the wire morphology. We attribute this change to decreased HCl sidewall etching. Attempts to grow pure Ge microwire arrays with Cu catalysts at lower temperatures (800 °C) resulted in similarly disordered growths.

The bulk crystallinity and phase purity of the wire arrays were characterized with X-ray diffraction. All wire arrays are epitaxial with respect to the substrate, but in some cases multiple Si₀.₉₋ₓGeₓ phases are observed. Two representative reciprocal space maps of wire arrays are presented in Figure 3. The reciprocal space map of the symmetric (111) peak of the highly tapered Si₀.₉₋ₓGe₀.₀₉ wires indicates the wires are single phase and epitaxial with respect to the Si (111) substrate (Figure 3a). A partial determination of the strain state of the

![Figure 2](image2.png)

Figure 2. Wire arrays grown at [Ge]/[IV] = 18.6% for an extended period of time as imaged with scanning electron microscopy. The wires have a highly faceted, tapered morphology that persists over long growth times with minimal catalyst volume left after longer growth times.

![Figure 3](image3.png)

Figure 3. High resolution X-ray diffraction reciprocal space maps. (a) Symmetric (111) peaks of the Si₀.₉₋ₓGe₀.₀₉ wire array. This map is qualitatively representative of all single-phase wire arrays. The wires are fully relaxed. (b) Symmetric (111) peaks of a Si₀.₉₋ₓGe₀.₁ wire array with a second Si₀.₉₋ₓGeₓ peak at lower intensity and higher Ge content. A similar effect is likely responsible for the wide wire bases, due to the difference in the surface energy between the catalyst particle and the SiO₂ growth mask during initiation compared to the vapor environment during growth. Reducing the Si₀.₉₋ₓGeₓ growth temperature to 950 °C was also attempted, and resulted in a large increase in disordered growth compared to perfect, vertical wire array growth, and we could not fairly compare its effects on the wire morphology. We attribute this change to decreased HCl sidewall etching. Attempts to grow pure Ge microwire arrays with Cu catalysts at lower temperatures (800 °C) resulted in similarly disordered growths.

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with and without an observed Ge-rich phase in X-ray diffraction were also defect free and phase pure, without any evidence of sidewall epitaxy.

However, closer examination of the wire tips reveals the presence of a region which is compositionally distinct from the bulk of the wires or the Cu catalyst, regardless of whether the wires appeared to be single phase or double phase in X-ray diffraction measurements (Figure 5). The presence of this phase is clearly seen in high angle annular dark field imaging as a region appearing brighter than the bulk of the wire, suggesting significant compositional contrast, and the interface between the two regions appears to be abrupt. Energy-dispersive X-ray spectroscopic mapping of Ge, Si, and Cu reveals significant Ge enrichment in these regions compared to the bulk of the wire, sharp interfaces between the two regions, and no Cu content except beyond the noise level at the catalyst location. Point and area spectra collected from the Ge-rich regions reveal compositions of Si$_{0.43}$Ge$_{0.57}$ and Si$_{0.22}$Ge$_{0.78}$, respectively. Scale bars below each map measure 50 nm.
phase wires, respectively. These observations imply that the catalyst is Ge-rich during the growth process, and the second phase forms either after precursor flow has ceased or as the sample is cooled.

As discussed earlier, the highest Ge content of 12% was achieved at [Ge]/[IV] = 18.6%, and attempts to grow with [Ge]/[IV] = 25.7% resulted in little to no growth. Our observations, namely, the tapering due to catalyst etching with increasing chlorogermane flow and evidence of a Ge-rich catalyst during growth, strongly suggest that Si$_{1-x}$Ge$_x$ wire arrays are limited to $\leq$12% Ge content when grown with Cu catalysts and chlorinated precursors at atmospheric pressure. There are several possible explanations for this. Examination of the Cu–Si and Cu–Ge phase diagrams shows a large difference in eutectic temperature (802 °C vs 640 °C) and a small difference in the solubility of Si and Ge in liquid Cu (31% vs 36%), both of which could play a role in the enrichment of the Cu catalyst and the reduction in growth rate due to a smaller driving force for nucleation of Ge-rich versus Si-rich alloys. Furthermore, at growth temperatures, the liquid catalyst droplet is saturated with Si before precursors are introduced because it is in direct contact with the Si substrate, which favors initial Si-rich supersaturation. Reducing the SiCl$_4$ flow rate while keeping the GeCl$_4$ flow rate constant did not result in higher compositional content. Once growth begins, the strain energy of nucleating alloy material with increased Ge content may play a key role in preventing higher Ge incorporation as the size of the wires leads to their strain relief behavior being more bulk-like. This would explain the Ge enrichment of the catalyst compared to the growing solid wire material.

**CONCLUSION**

In conclusion, vertically aligned Si$_{1-x}$Ge$_x$ wire arrays were grown with atmospheric pressure, Cu-catalyzed vapor–liquid–solid growth with chlorosilane and chlorogermane precursors over 1 cm$^2$ areas. Increased GeCl$_4$ flow rate led to Ge incorporation of up to 12% with a large reduction in the growth rate. The highly faceted, tapered morphology was linked to in situ evaporation and etching of the catalyst particle. Ge-rich epitaxial crystals were found near the tips of the microwires, leading us to speculate that the catalyst is Ge-rich during the growth process. Alternative catalyst materials, such as Au, or precursor choices could allow for increased Ge content, although their effect on the electronic quality and uniformity of the array growth are negative and unknown, respectively. Si$_{0.89}$Ge$_{0.11}$ microwires should allow for lattice and coefficient of thermal expansion matched growth of GaP to allow for integration of III–V materials for photovoltaic and optoelectronic applications.

**ASSOCIATED CONTENT**

1. Supporting Information

SEM images of attempts to grow high Ge content wire arrays, SEM images of Si wire arrays grown at high temperature, reactor schematic, and photocconductivity measurements are included in the Supporting Information. The Supporting Information is available free of charge on the ACS Publications website at DOI: 10.1021/acs.cgd.5b00097.

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**Author Contributions**

The manuscript was written through contributions of all authors. All authors have given approval to the final version of the manuscript. Christopher T. Chen and Hal S. Emmer contributed equally.

**Notes**

The authors declare no competing financial interest.

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